

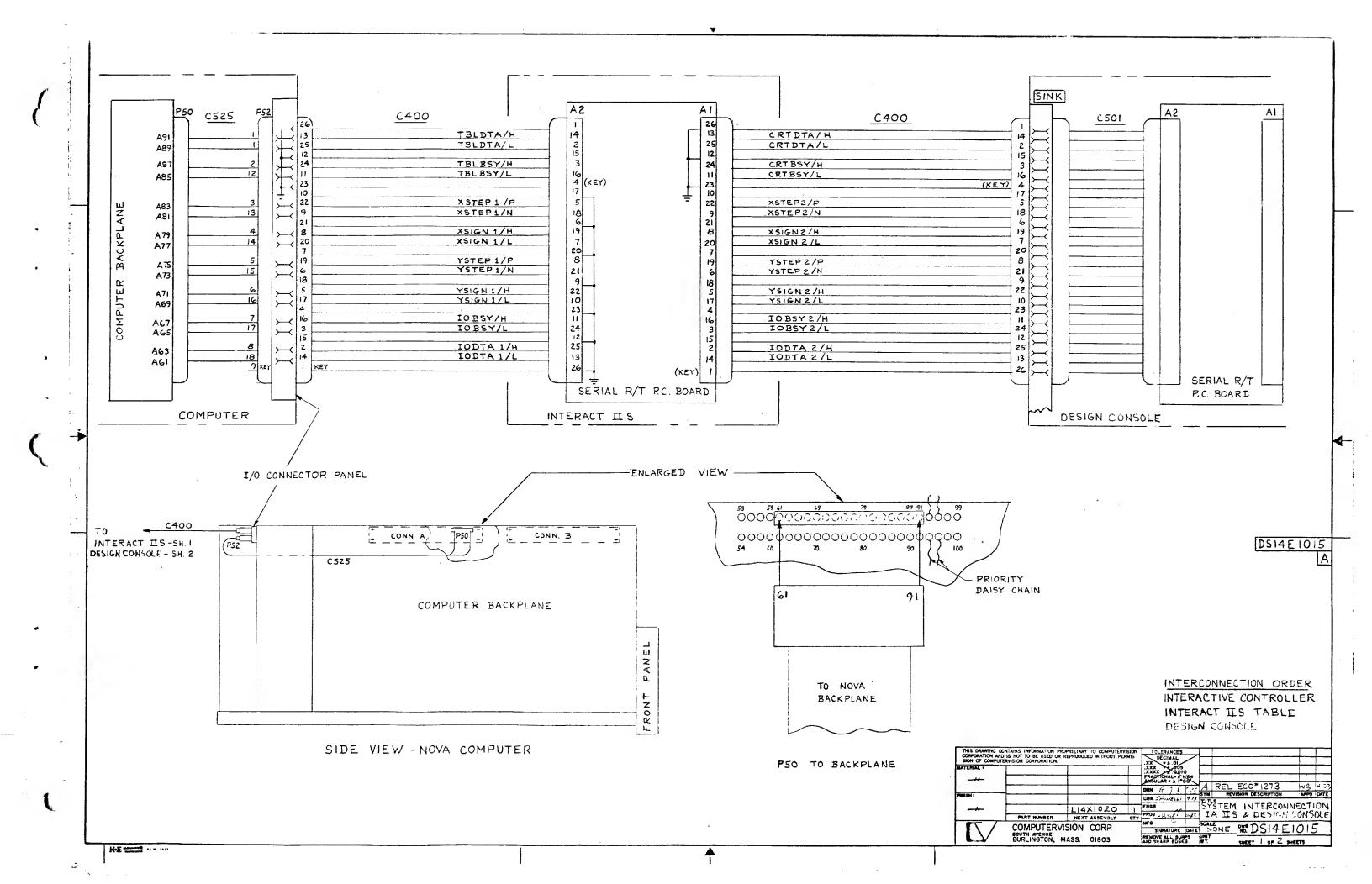
## **IOS Controller**

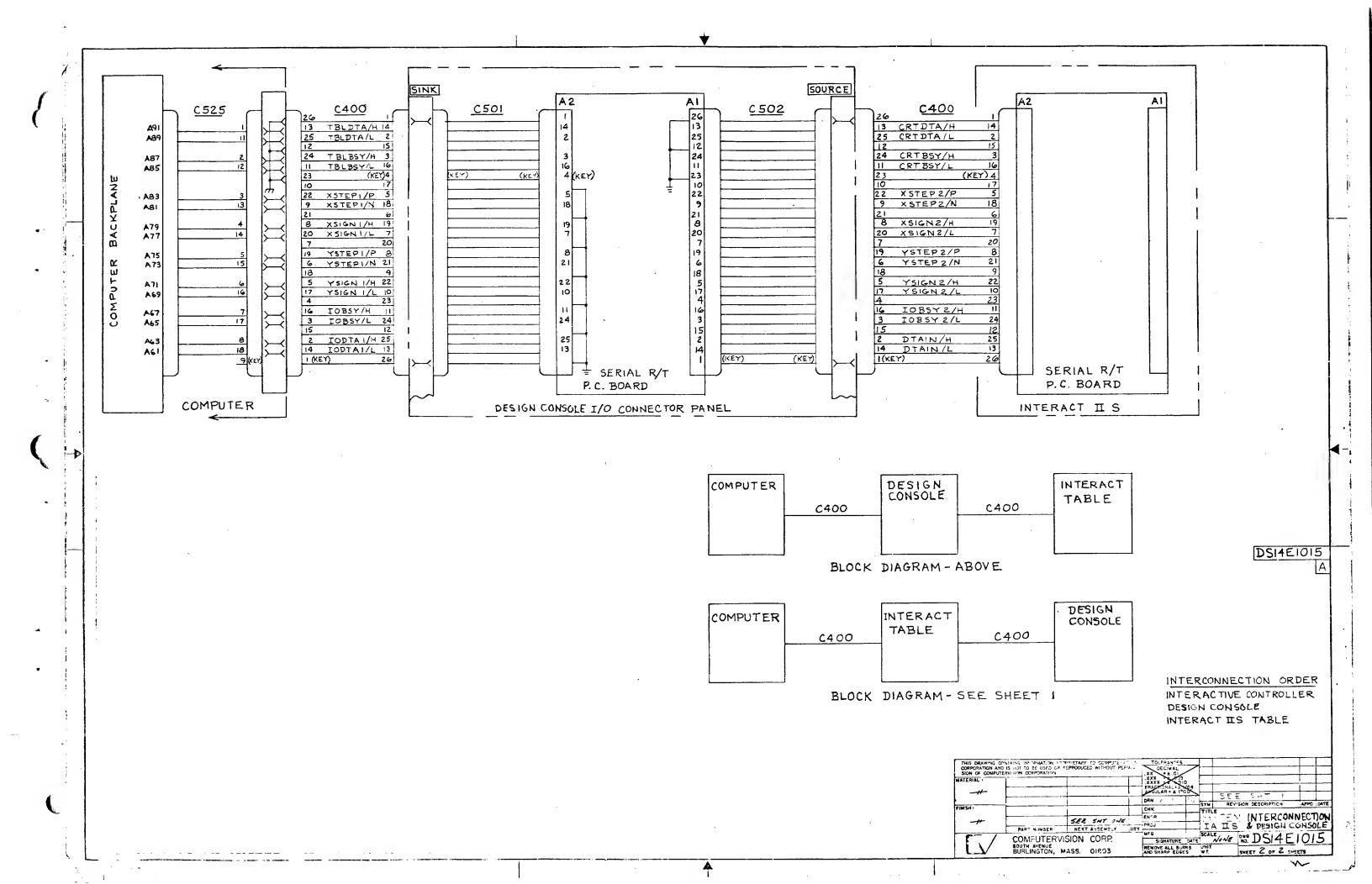
Logic Diagrams

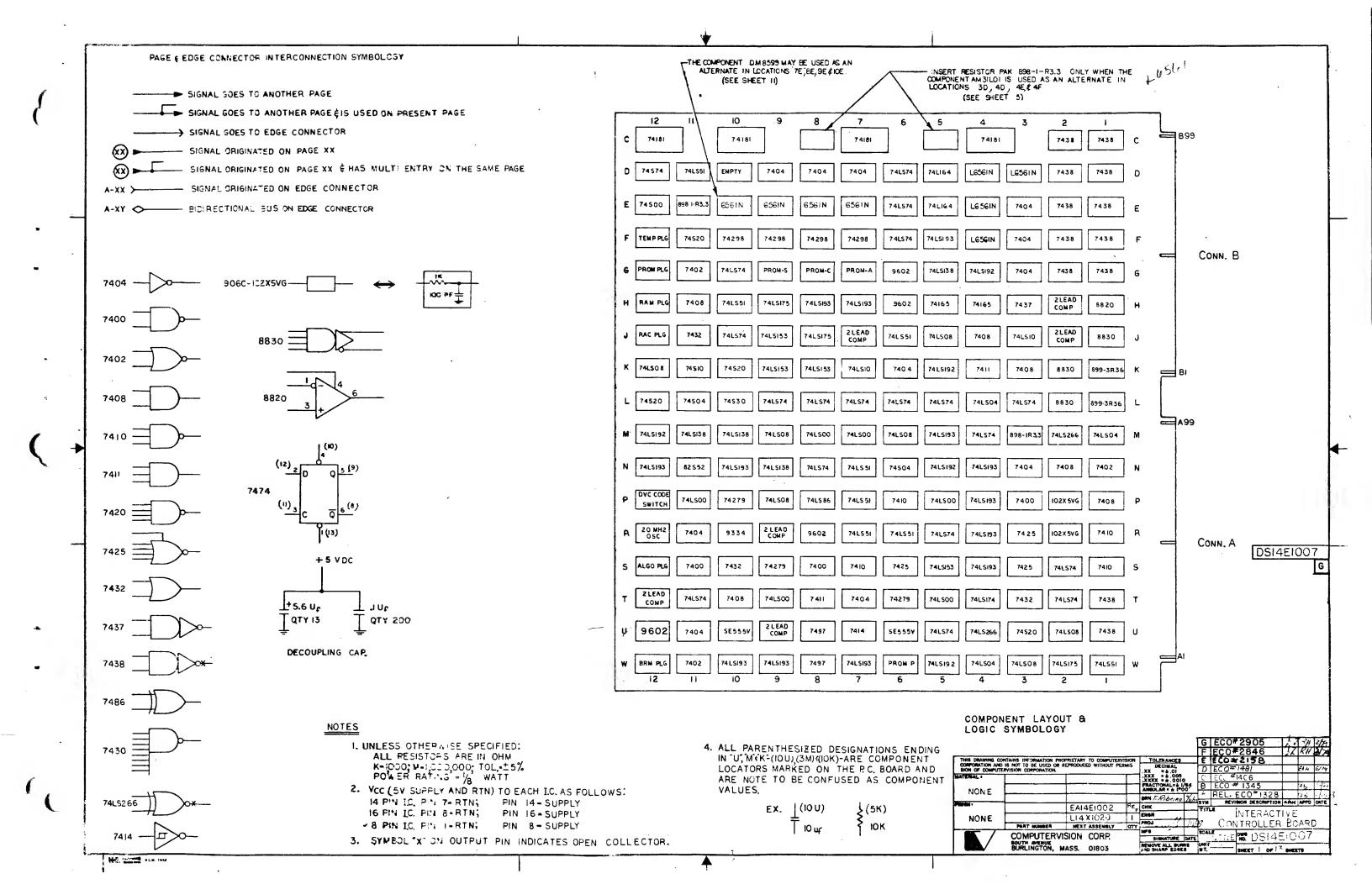
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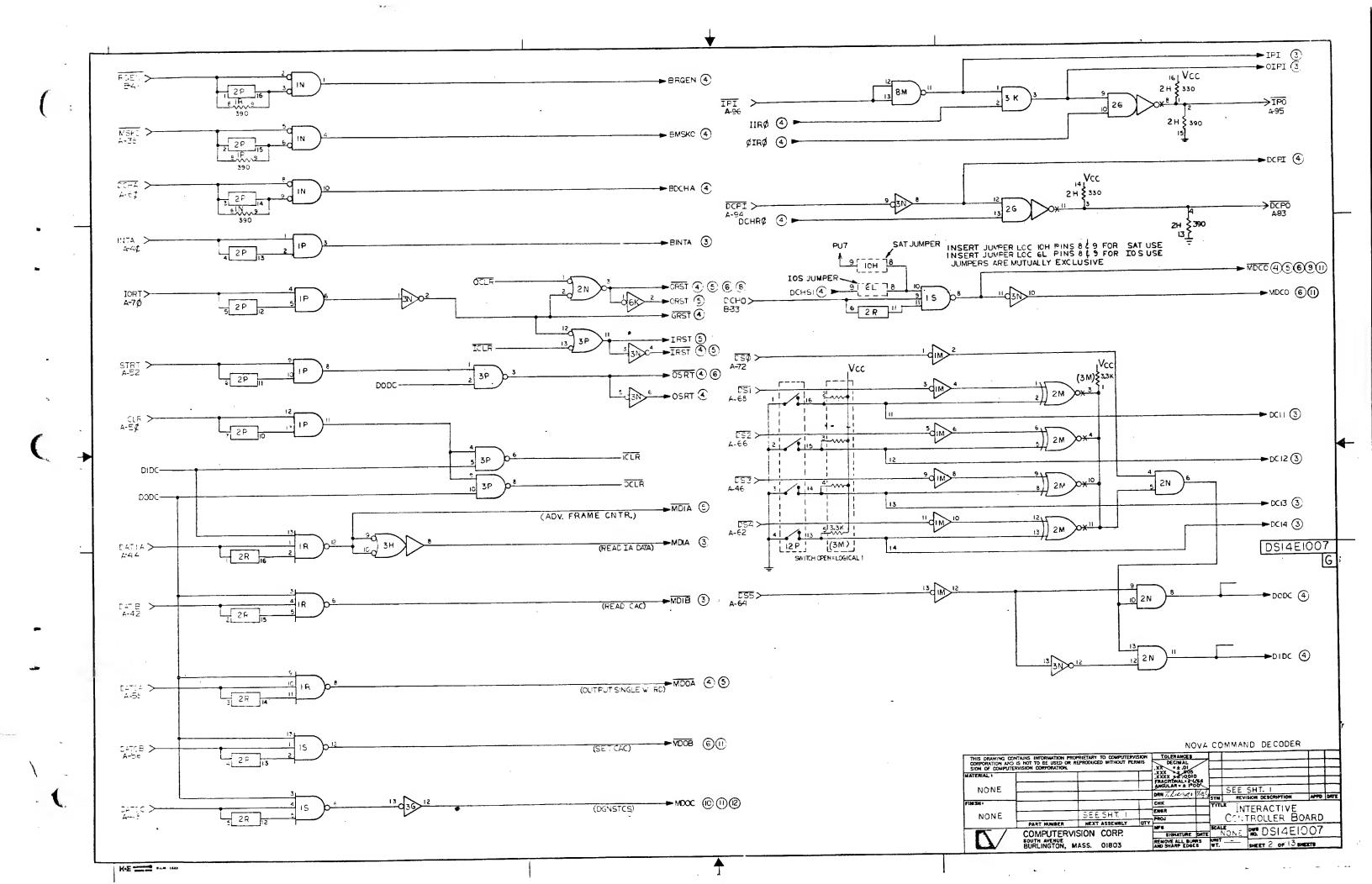
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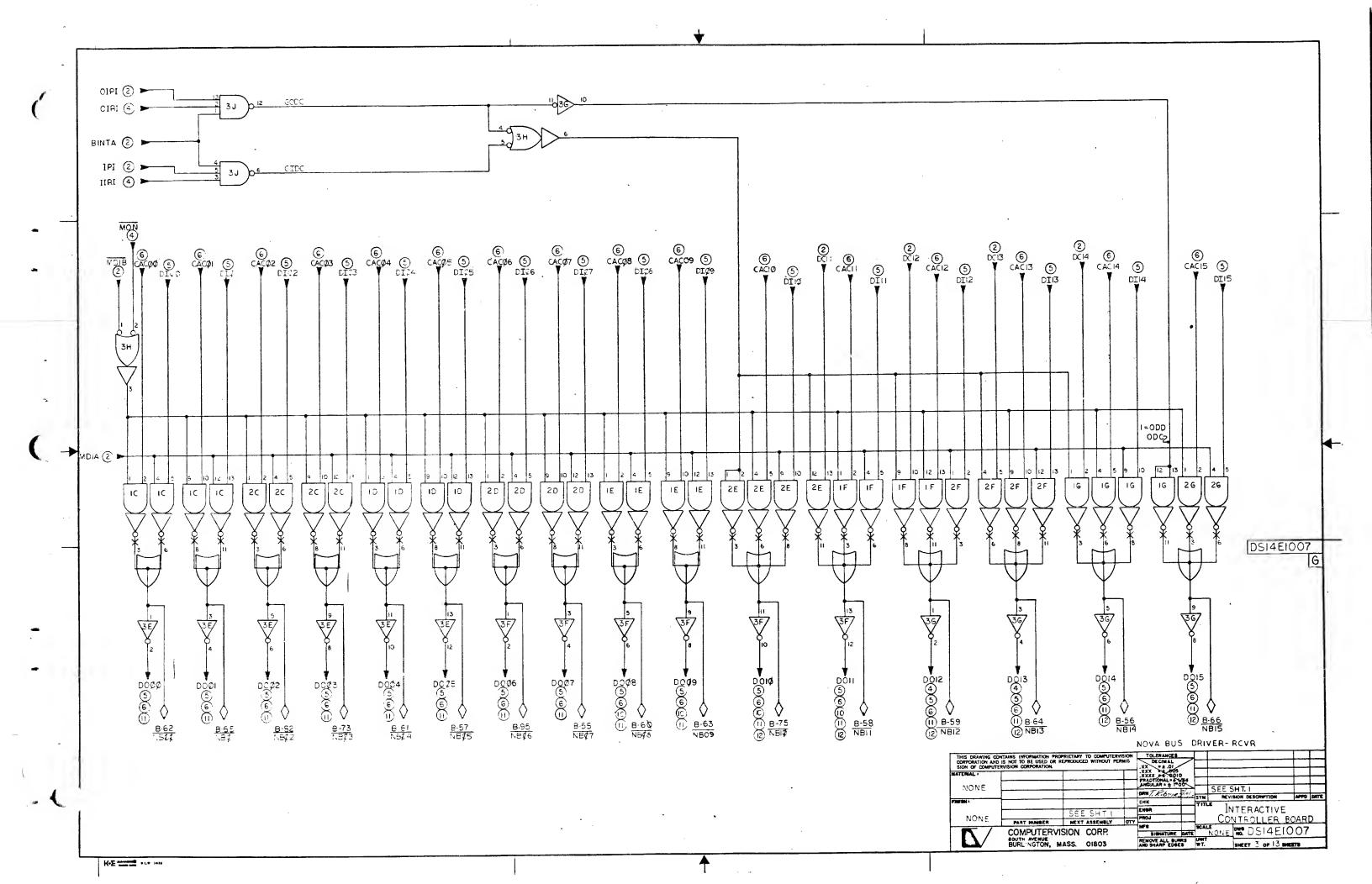
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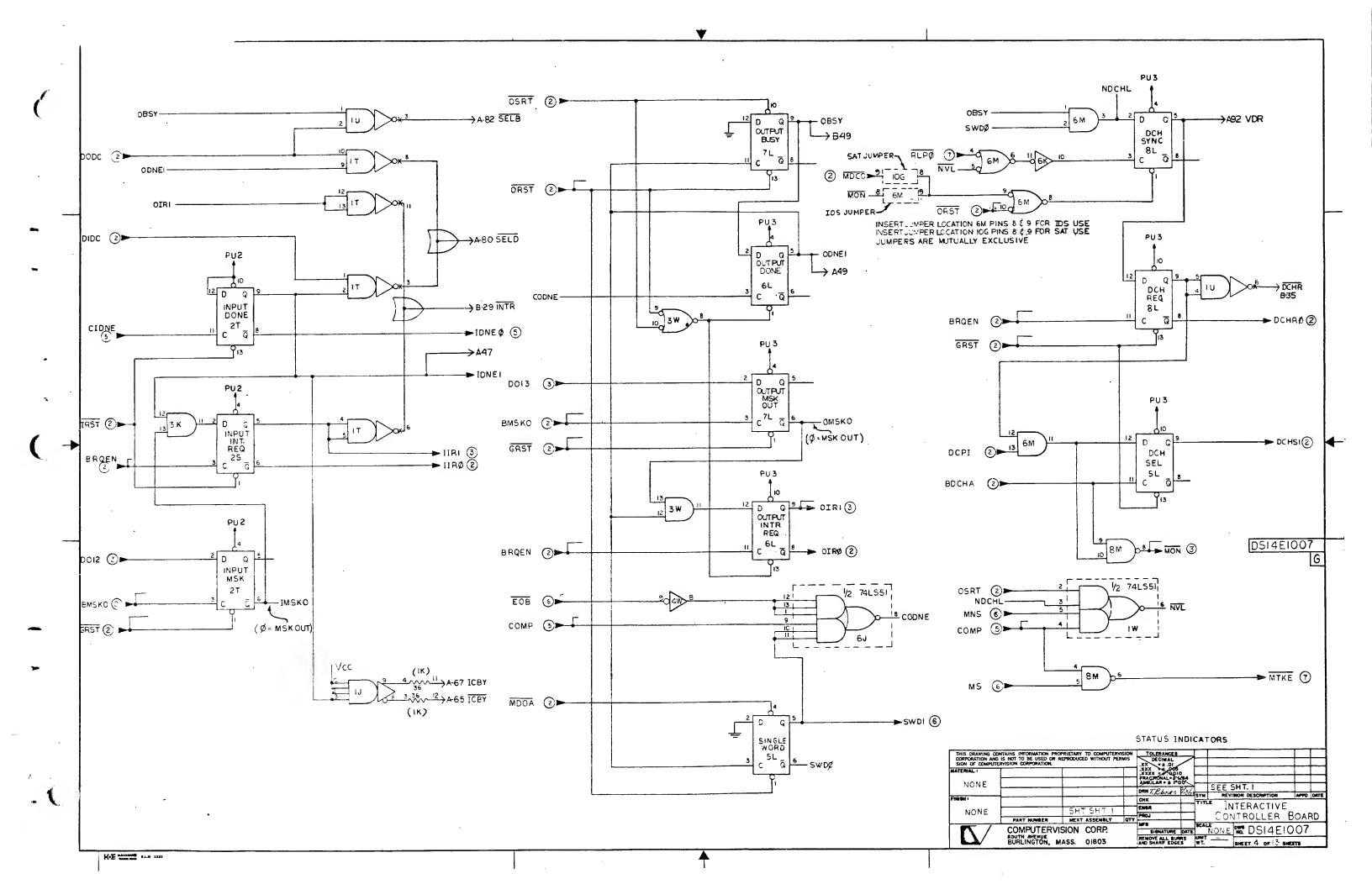


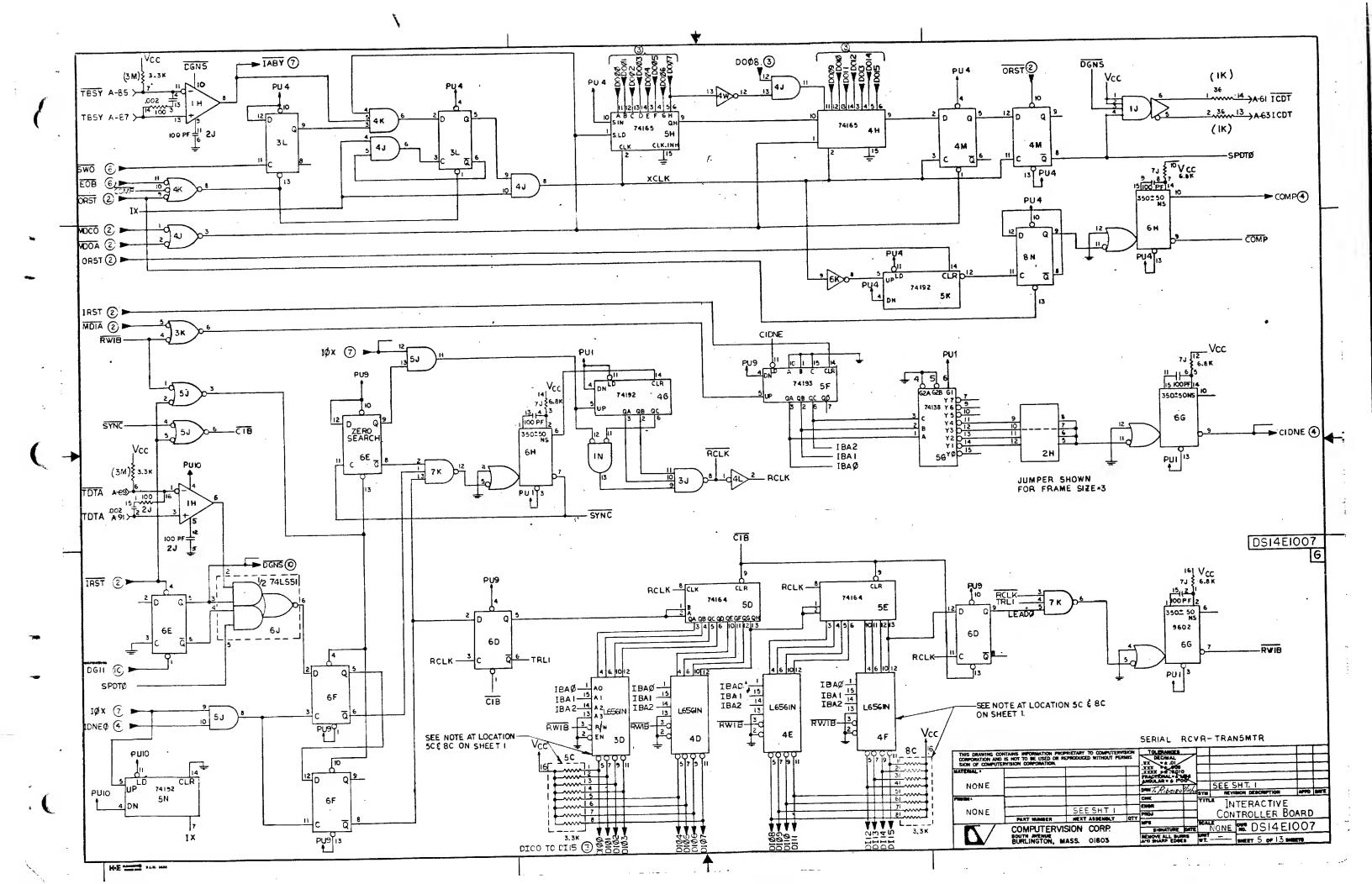


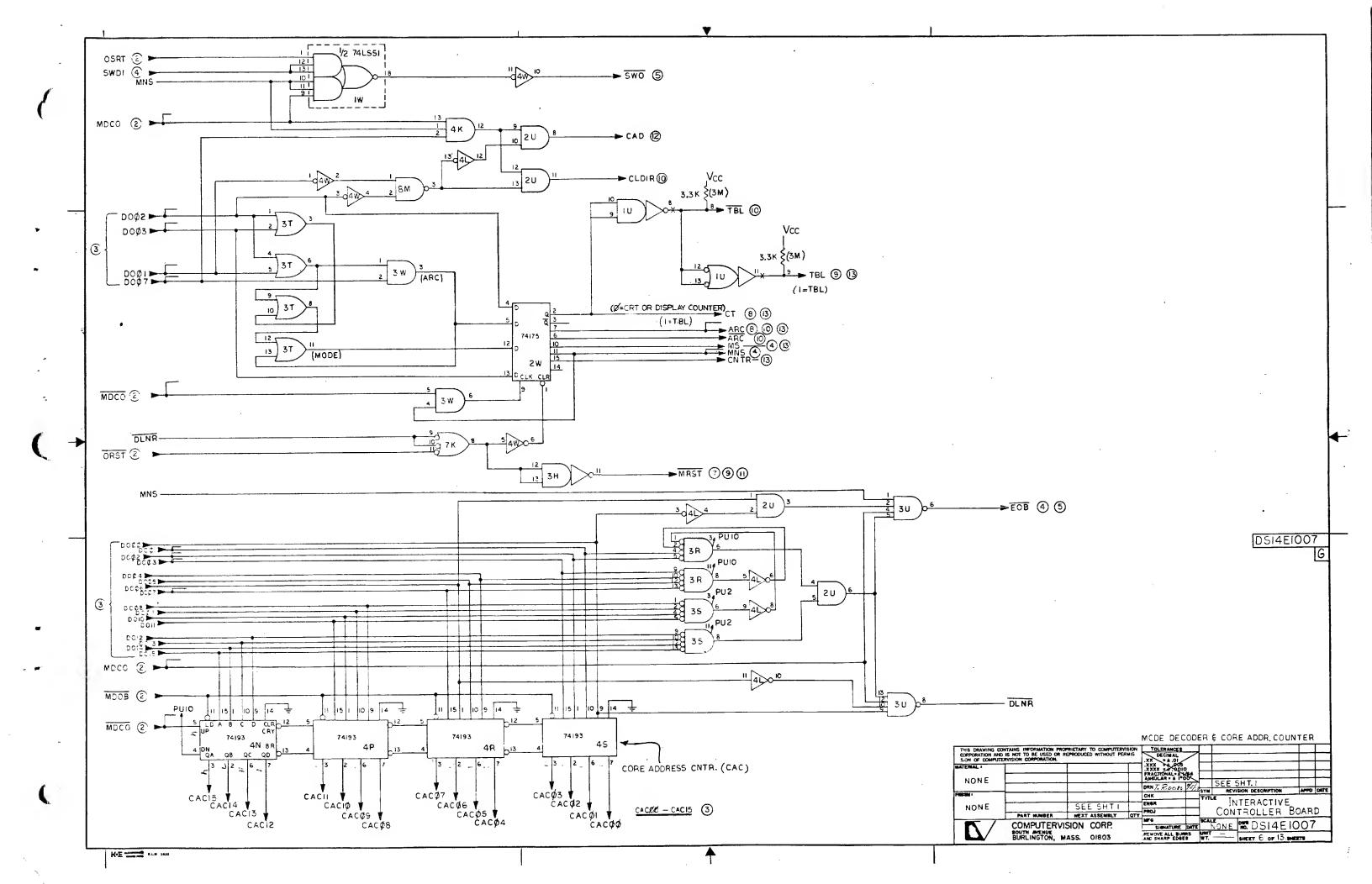


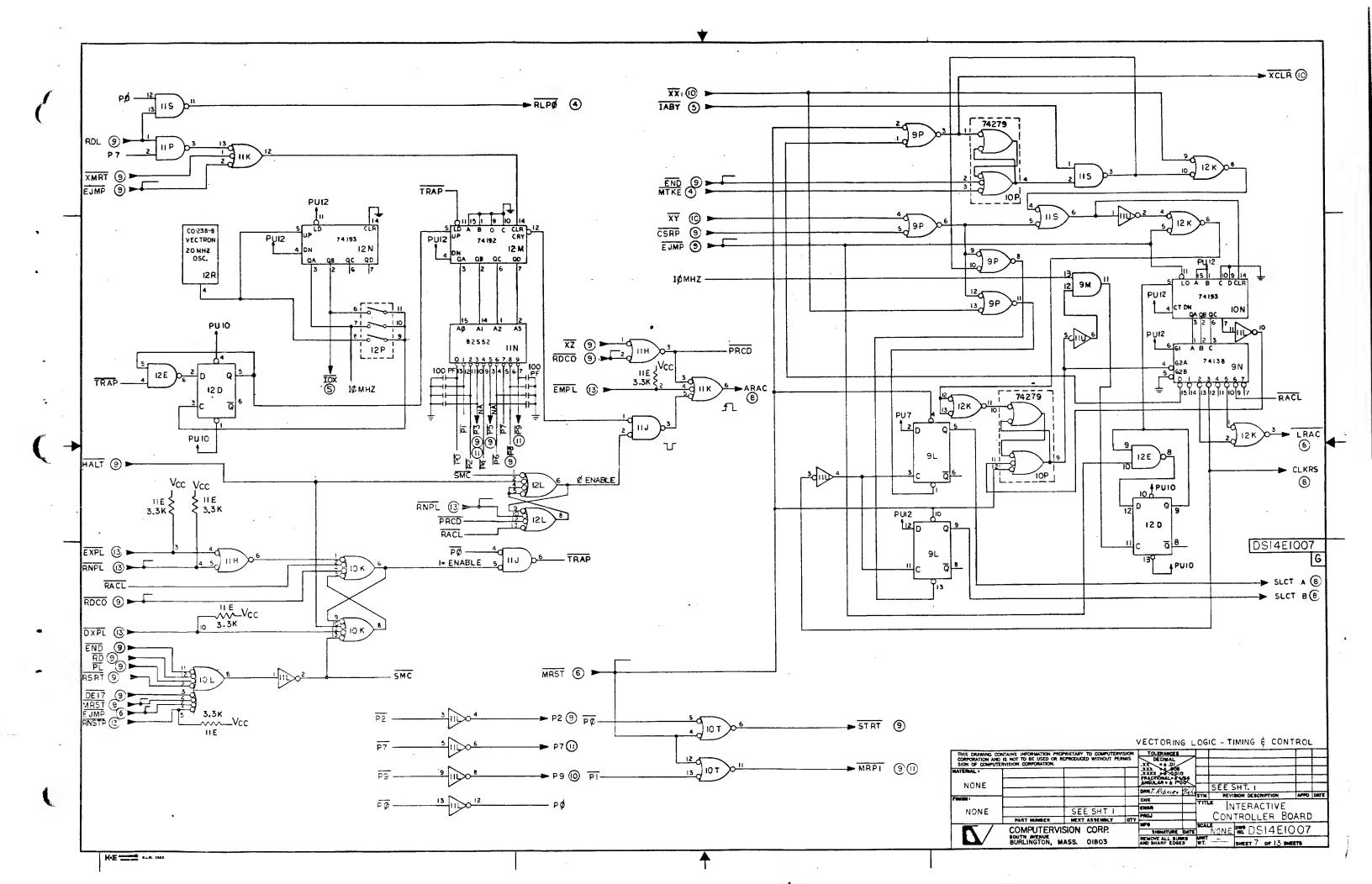


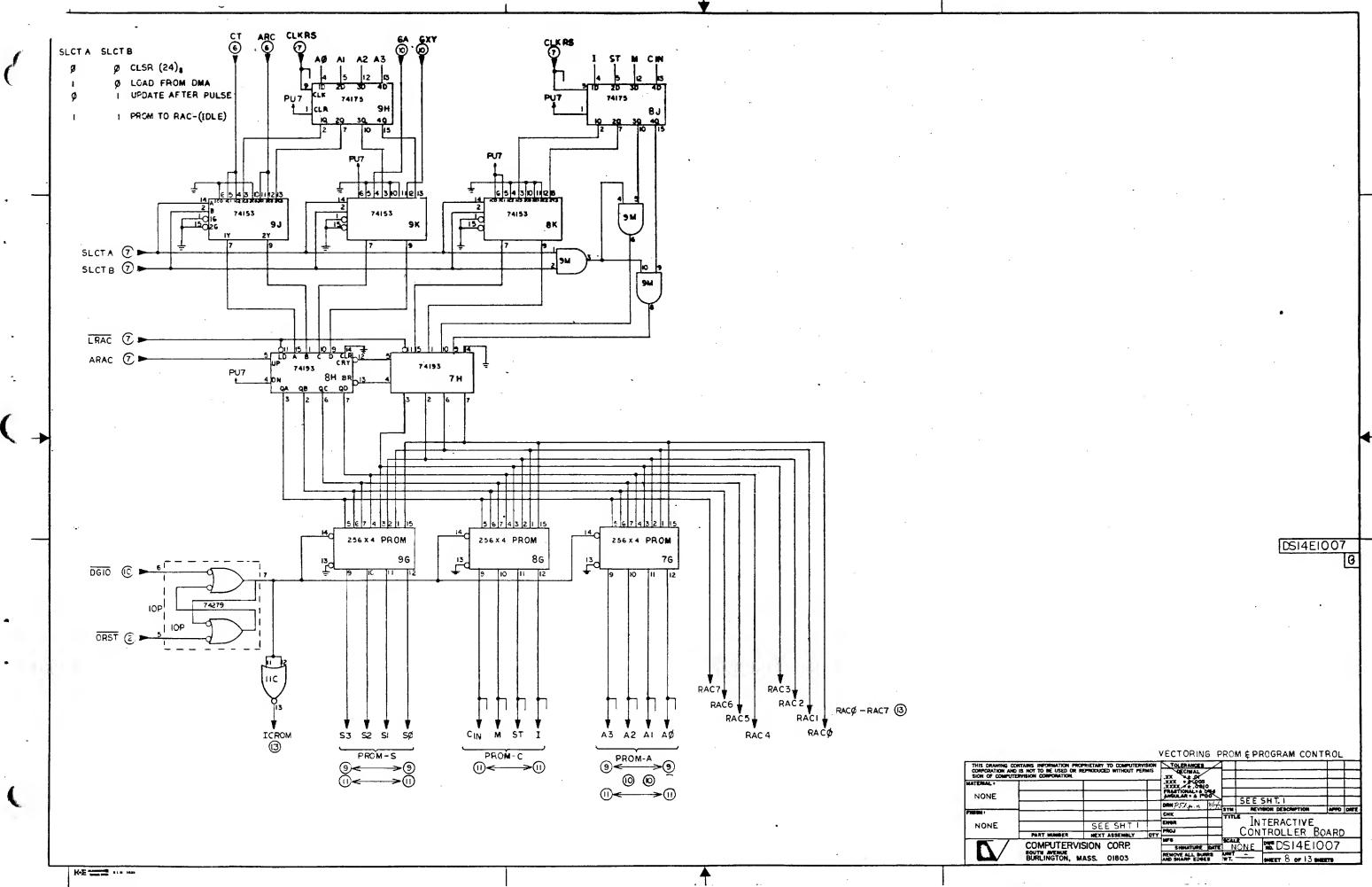




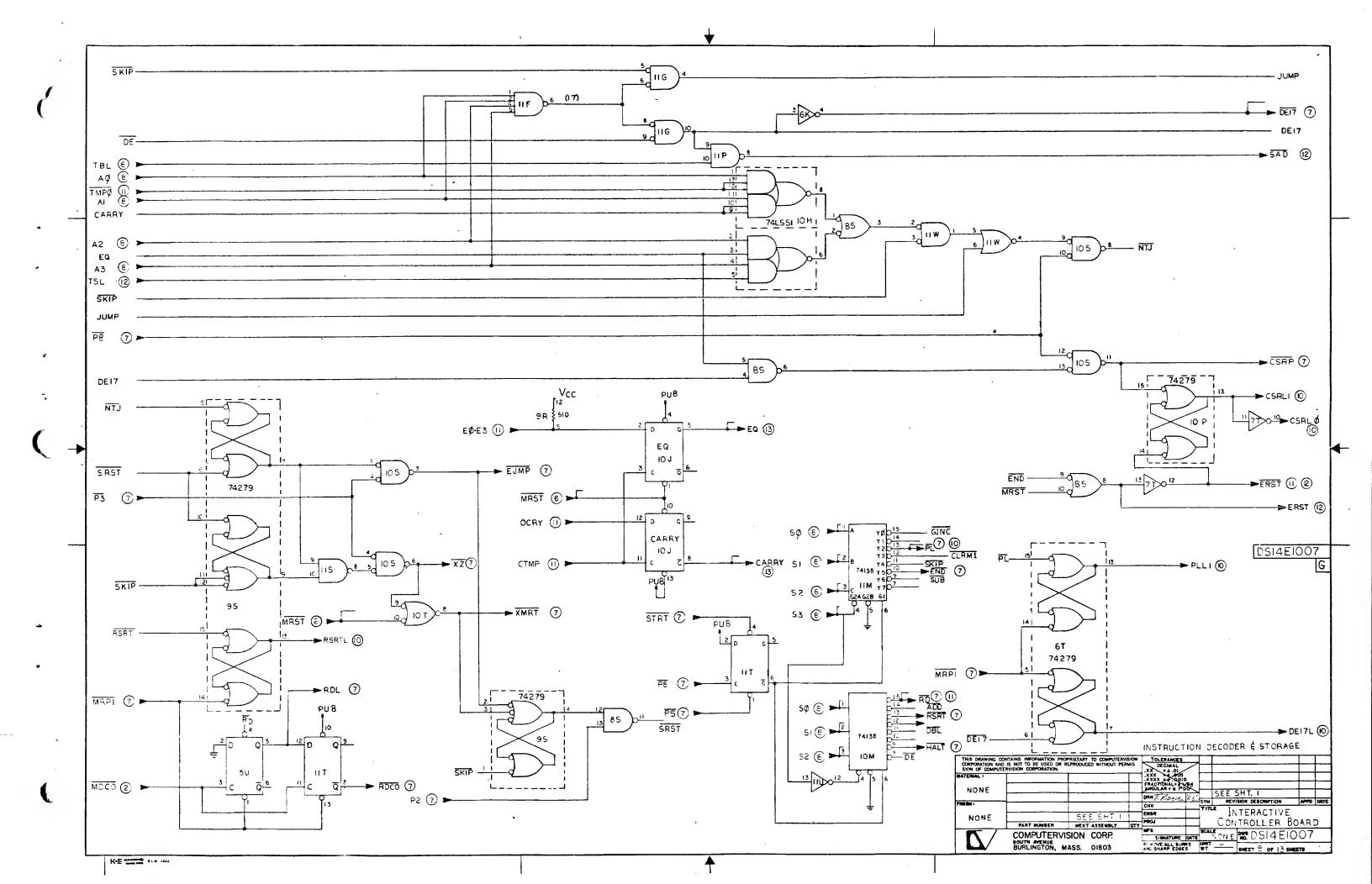


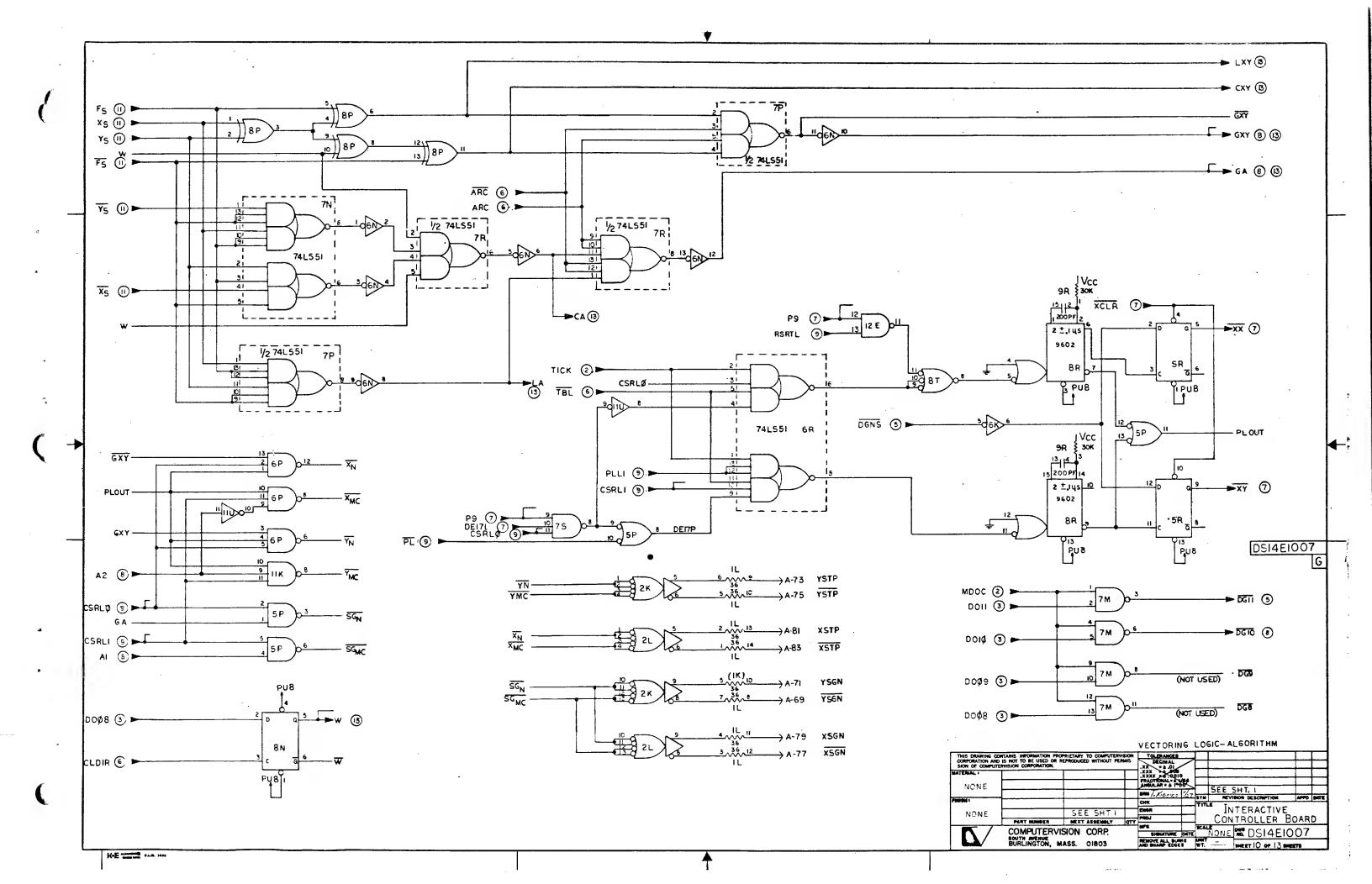


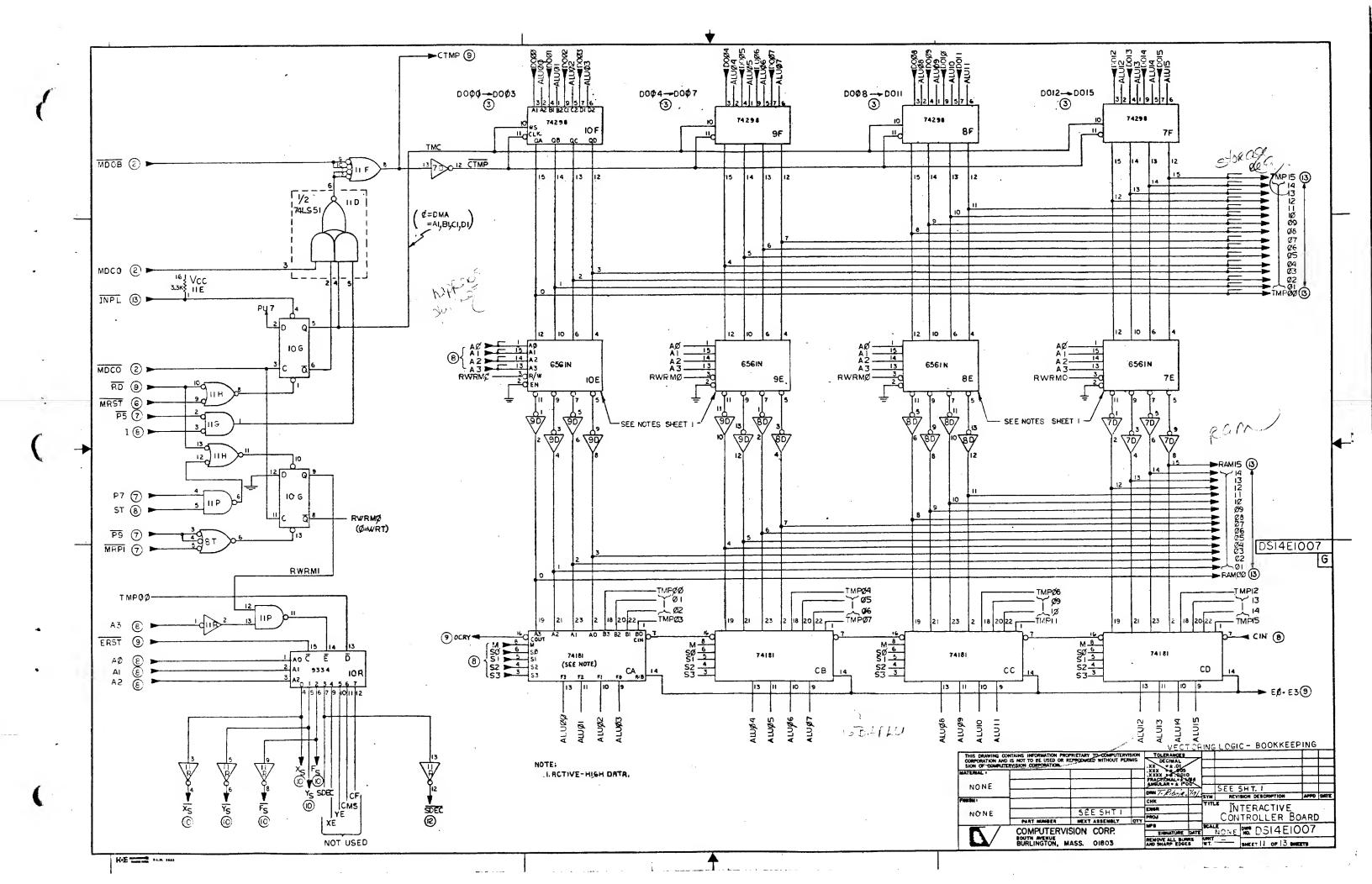


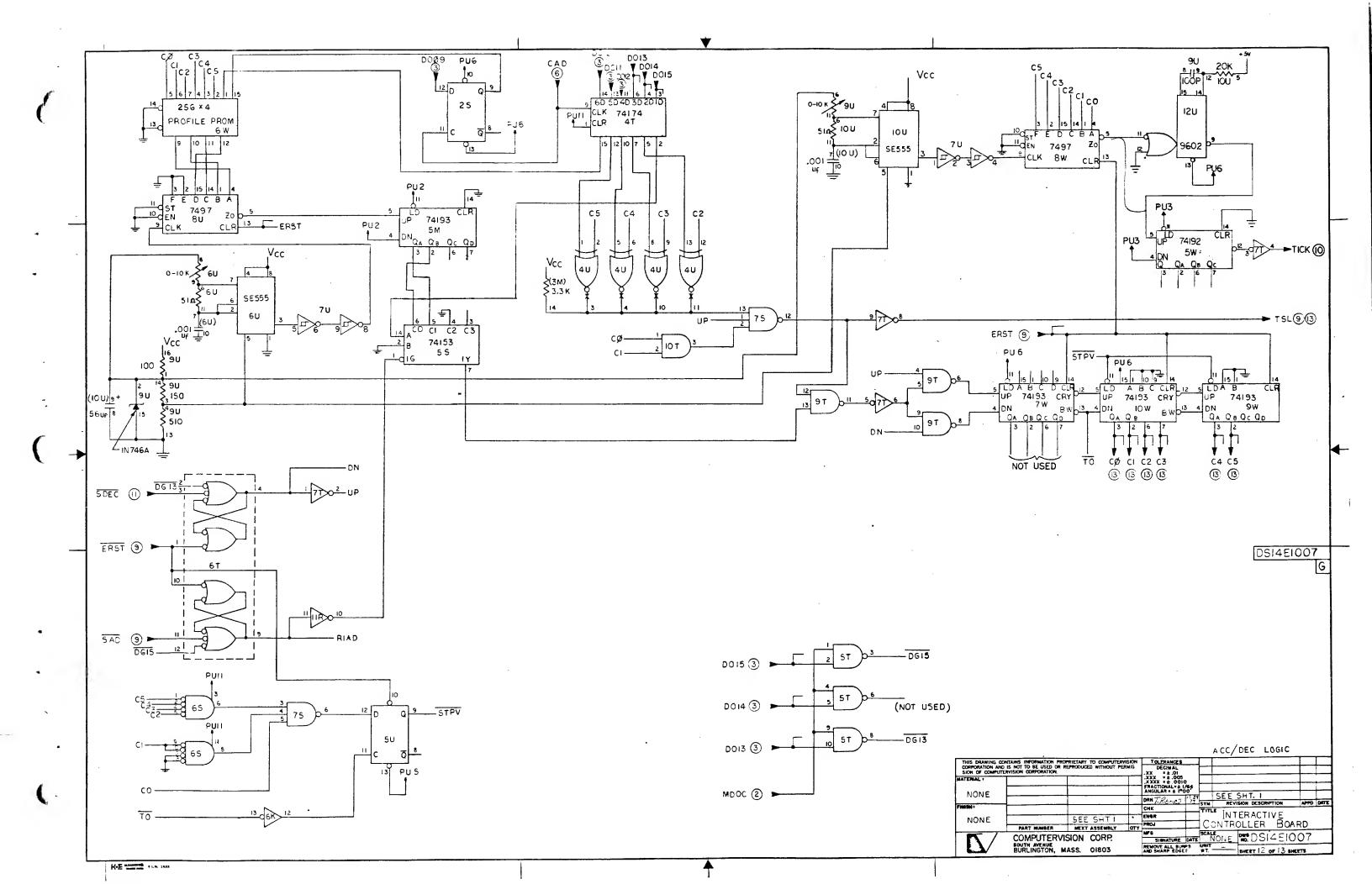


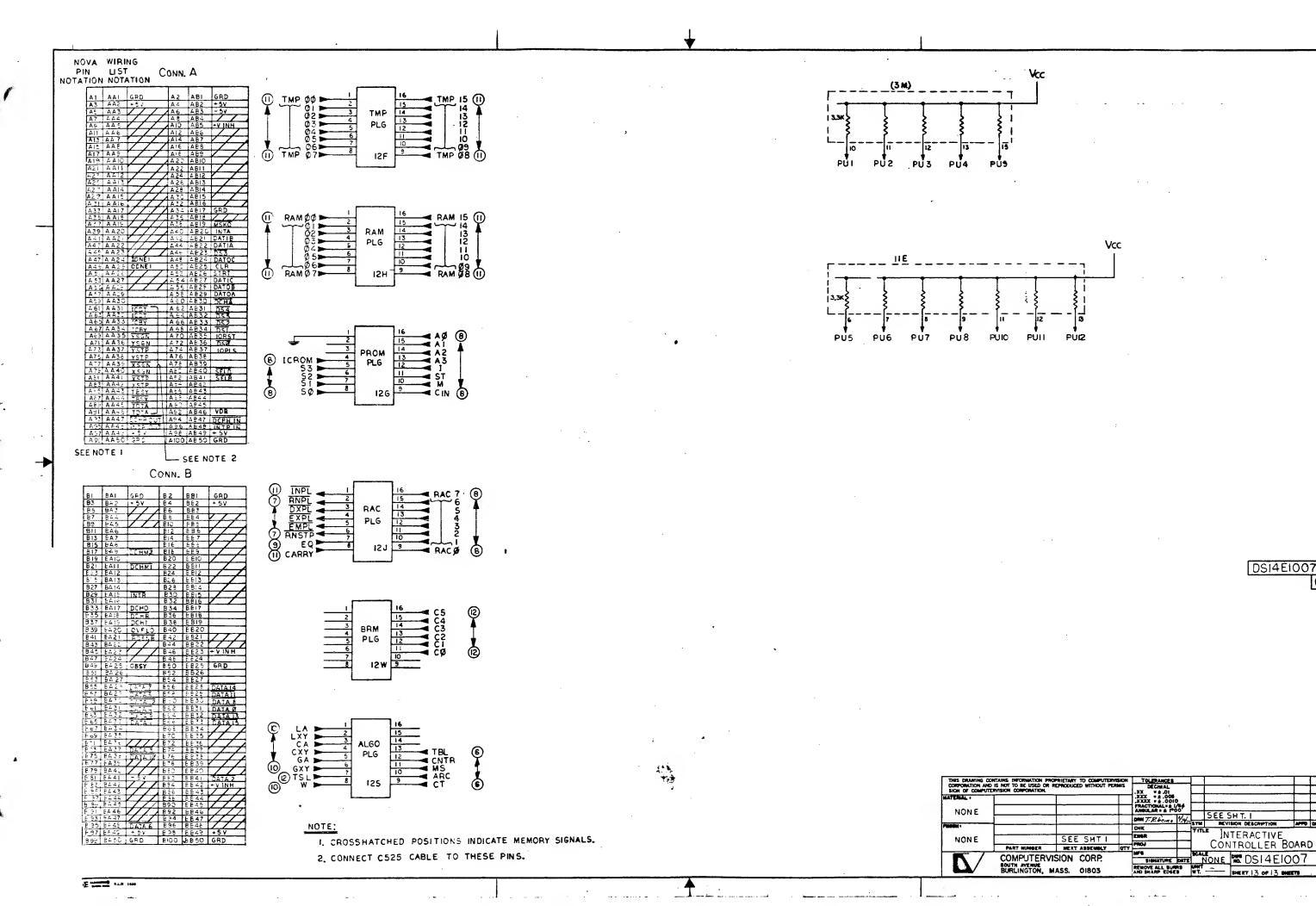
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INTERACTIVE CONTROLLER BD. COMPUTERIZED ALPHA SIGNAL NAME LIST.

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0001 MAIN
                         ; INTERACTIVE CONTROLLER BD
91
                        ALFHA SIGNAL NAMES
02
                        , AW14E 1012 REV A
\odot
64
                                 GEN. OUTPUT DEVICE CODE.
05 00000 000002 GODC:
06 00001 000003 GIDC.
                        Ξ.
                                JGEN, INPUT DEVICE CODE.
                                NOVA BUS BIT 15
07 00002 000005 NB15. : 5.
                                ) NOVA BUS BIT 14
08 00000 000006 NB14..
09 00004 000007 NB13. . 7.
                                ONOVA BUS BIT 13
10 00005 000010 NE12. . 8.
                                , NOVA BUS BIT 12
11 00006 000011 NB11. . 9.
                                ; NOVA BUS BIT 11
                                NOVA BUS BIT 10
12 00007 000012 NB10 . 10.
12 00010 000012 NB09. 11.
                                NOVA BUS BIT 09
14 00011 000014 NB05. . 12
                                , NOVA BUS BIT 08
15 00012 000015 NB07. . 13.
                                NOVA BUS BIT 07
                                , NOVA BUS BIT 06
16 90913 900916 NB06. . 14.
17 80014 800917 NB05. . 15.
                                NOVA BUS BIT 05
                                .NOVA BUS BIT 04
18 00015 000020 NE04. . 16.
19 00016 000021 NB93. . 17.
                                , NOVA BUS BIT 03
                                . NOVA BUS BIT 02
20 00017 000022 NE02 : 18
21 00020 000023 NB01. . 19.
                                - NOVA BUS BIT 01
22 00021 000024 NB00. . 20.
                                . NOVA BUS BIT 00
22 00022 000025 CIPI.
                                .OUTPUT INTERBUFT PRIORITY IN.
                                > BUFFERED INTERRUFT ACKNOWLEDGE (INTA)
24 00023 000026 BINTA.
                        22.
25 00024 000027 OIR1.
                                GOUTPUT INTERRUPT REQUEST (1).
                                , INPUT INTERRUPT PRIORITY IN
26 00025 000010 IPI.
27 00026 000031 IIR1.
                                . INFUT INTERRUFT REQUEST (1).
28 00027 000032 MDIB. . 26.
                                DAY DATA IN B. PUTS CORE ADDRESS COUNTER ON
                                 THE BUS.
29
                                - MEMORY ON PUTS CORE ADDRESS COUNTER ON
30 00030 000032 MON. .
                                THE BUS DURING DMA OF.
31
                                 , MY DATA IN A. PUTS INTERACT DATA ON THE BUS.
32 00011 000034 MDIA
                         28.
33 00032 000035 D000.
                                ) DATA OUT. ROYD DATA FROM CPU, BUFFERED AND
                                . INVERTED.
35 00003 000036 D001.
                         ΞΘ.
                         31
36 00034 000037 D002.
                         32
37 00035 000040 DO03.
38 00036 000041 D004.
39 00037 000042 0005
40 00040 000043 0006:
41 00041 000044 DO07:
                        37.
42 00042 000045 DC08.
43 00043 000046 0009
44 00044 000047 0010:
45 00045 000050 D011:
                        40.
46 00046 000051 D012.
                        41.
47 00047 000052 D013
                        42.
48 00050 000053 DO14.
49 00051 000054 D015
                              , ; DATA GUT.
                        44.
50 00052 000055 DC11.
                                DEVICE CODE.
                        45.
51 00053 000056 DC12:
                        4E.
52 00054 000057 DC13
                        47.
53 00055 000060 DC14:
                        48.
-54 00056 000061 CAC00.
                        49.
                                ...CORE ADDRESS COUNTER (CAC) MSE.
55 00057 000062 CAC01.
                        50.
56 00060 000063 CAC02.
                        51.
57 00061 000064 CAC03.
                        52.
                                                                             REV
58 00062 000065 CAC04:
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59 00063 000066 CAC05.

AWI4E1012

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0002 . MAIN
01 00064 000967 CAC06:
02 00065 000070 CAC07.
03 00066 000071 CAC08:
04 00067 000072 CAC09:
05 00070 000073 CAC10.
                         59.
06 00071 000074 CAC11:
07 00072 000075 CAC12:
08 00072 000076 CAC13:
09 00074 000077 CAC14:
                         63.
                                 , CORE ADDRESS COUNTER. LSB.
10 00075 000100 CAC15.
                         €4.
                                 DATA IN ROYD INTERACT DATA. MSB
11 00076 000101 DI00:
12 00077 000102 DI01.
                         66.
13 00100 000103 DI02.
                         €7.
14 00101 000104 DIOD.
                         -68.
15 00102 000105 DI04:
16 00102 000106 DI05:
17 00104 000107 DI06.
                         72.
18 00105 000110 DI97
19 00106 000111 DIOS.
20 00107 000112 DI09.
                         74.
21 00110 000113 DI10.
22 00111 000114 DI11
23 00112 000115 DI12
24 00113 000116 DI13
                         78.
25 00114 000117 DI14:
                         79.
                                 , DATA IN. LSB.
26 00115 000120 DI15
                         80.
                                 FREGUEST ENABLE OFU GENERATED SIGNAL (ROENB)
                         81
27 00116 000121 ROEN. .
                                 , MASK OUT. CPU GENERATED SIGNAL
28 00117 000123 MSKO.: 33.
                                 JOATA CHANNEL ACKNOWLEDGE. CPU GENERATED
29 00120 000125 DCHA.. 85.
                         , SIGNAL
30
                                 FINTERRUPT ACKNOWLEDGE. CPU GENERATED
                        87.
31 00121 000127 INTAX
                                 SIGNAL: (INTA)
32
                                 , I/ORST. CPU GENERATED SIGNAL
DB 00122 000131 IGRT:
                         89.
                         91.
                                 START, CPU GENERATED SIGNAL.
34 00123 000133 STRT:
                         93.
                                 CLEAR CPU GENERATED SIGNAL
35 00124 000135 CLR
                                 DATA INPUT A. CPU GENERATED SIGNAL USED FOR
36 00125 000137 DATIA: \ 95.
                                 ;READING DATA.
____
                                 ; DATA INPUT B. CPU GENERATED SIGNAL USED FOR
38 00126 000141 DATIB. 97.
                                 FREADING CAC.
39
                                 , DATA OUTPUT A. CPU GENERATED SIGNAL. USED FOR
40 00127 000143 DATOA:
                                 JSINGLE CHARACTER OUTPUT.
41
                                 DATA OUTPUT B. CPU GENERATED SIGNAL. USED FOR
42 00130 000145 DATOB.
                         101.
                                 , SETTING CAC.
43
                                 , DATA OUTPUT C. CPU GENERATED SIGNAL. USED FOR
44 00131 000147 DATOC.
                                 🦪 DIAG.
45
                                 ; INTERRUPT PRIORITY IN. CPU GENERATED SIGNAL
46 00132 000151 IPI.:
                         105.
                                 DATA CHANNEL PRIGRITY IN. CPU GENERATED
47 00133 000152 DCFI..
                         196.
                                 SIGNAL
48
                                 DATA CHANNEL OUT. CPU GENERATED SIGNAL
49 00134 000153 DCHO
                         107.
                                 , DEVICE SELECT. CPU GENERATED SIGNAL. MSB.
50 00135 000155 DS0:
                         109.
51 00136 000156 DS1:
                         110.
52 00137 000157 DS2:
                         111.
53 00140 000160 DS3.
                         112
54 00141 000161 DS4:
                         113.
                                 DEVICE SELECT. LSB.
55 00142 000162 DS5:
                         114.
                                 ; INTERRUPT PRIORITY OUT.
56 00143 000163 IPO.:
                         115.
                                 DATA CHANNEL PRIORITY OUT.
                         116.
57 00144 000164 DCPO.
                                 ; BUFFERED REG. ENABLE.
58 00145 000202 BRQEN. 130.
                                                                     AWI4E1012 A
```

BUFFERED MSKO.

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59 00146 000203 BMSKO. 131.

```
0003 MAIN
  <u>@1 00147 000204 BDCHA. 132</u>
                                  , BUFFERED DOHA
    00150 000205 GRST.: 133.
                                  OUTPUT RESET.
                                  GENERAL RESET, BUFFERED IORST
  .5 00151 000206 GRST.: 124.
                                  JINPUT RESET.
  04 00152 000207 IRST.
                         135.
  05 00153 000210 IRST.: 136.
                                  , OUTPUT START.
  06 00154 000211 OSRT.: 137.
                           138.
  07 00155 000212 OSRT.
                                  : INFUT CLR.
  08 00156 000213 ICLR.
                           139.
  09 00157 000214 OCLR
                           140.
                                  , OUTFUT CLR
                                  JMY DATA INPUT A. DATIA WITH DEVICE CODE
  10 00160 000215 MDIA.
                           141
                          142.
                                  , MY DATA OUTPUT A. DATOA WITH DEVICE CODE.
  11 00161 000216 MDOA.:
                                  HAMP DATA OUTPUT B. DATOB WITH DEVICE CODE.
. 12 00162 000217 MDOS.
                           143.
                                  , MY DATA OUTPUT C. DATOC WITH DEVICE CODE.
                           144.
  <u>13 00163 000220 MDCC.</u>
                           145.
                                   , INFUT INTERRUPT REG (0).
  14 00164 000221 IIRO
, 15 00165 000222 OIR0:
                                  ,OUTPUT INTERRUPT REQ (0).
                          146.
                          147.
  16 00166 000223 DCHR0
                                  , DATA CHANNEL REQ (0).
                                   DATA CHANNEL SYNC (1).
  17 00167 000224 DCHS1:
                          148.
                                   ) DATA CHANNEL PRIORITY IN: BUFFERED DAISY
 18 00170 000225 DCFI.
                           149.
                                   JOHAIN INFUT.
  19
                           150
                                  , MY DATA CHANNEL GUT
  20 00171 000226 MDCO.
  21 00172 000227 MDCO.
                          151
                          152.
                                  DECODED IMPUT DEVICE CODE
 ·22 00173 000230 DIDC.
                                   DECODED OUTPUT DEVICE CODE
                          153.
  23 00174 000231 DGDC.
                                   JRYW INPUT BUFFER! MFERS INTERACT DATA FROM
  24 00175 000233 RWIB.
                                   SERIAL TO PARALLEL CONVERTER INTO RAM.
  26 00176 000236 CIB:
                                   CLR INFUT BUFFER.
  27 00177 000227 SYNC.
                          159.
                                   , INDICATES START OF NMITION
                                  FROV CLK.
                          180.
  78 00200 000264 RCLK.
                          131
  9 00201 000265 RCLK.:
                                  TRAIL 1, LAST BIT IN THE RECEIVED WORD
 🔪 0 00202 000266 TRL1.
                           182
  I1
                                  , INPUT BUFFER ADDRESS 0.
 32 00203 000271 IBA0:
                          185.
                          186.
 33 00204 000272 IBA1.
                                  JINPUT BUFFER ADDRESS 1
 34 00205 000273 IBA2.
                          187.
                                  IMPUT BUFFER ADDRESS 2.
  35 00206 000314 LEADO:
                                  FIRST BIT IN THE RECEIVED WORD.
                          204
  36 00207 000315 IOX:
                           295.
                           206.
                                  JINPUT DONE. INPUT DONE F/F (0).
  37 00210 000316 IDNE0
                           207.
                                  GROVD DATA, ROYD FROM IA OR CRT.
 38 00211 000317 RCDT.
                                  DIAGNOSTICS WHITER CONN. TO ROVE
                          208.
 39 00212 000320 DGMS. :
 40 00213 000321 CIDNE: 209.
                                   GOLK INPUT DONE.
 41 00214 000022 DG11
                                   DOBNITOS 11. SIGNAL WHICH CONNECTS
                                   , WMITER TO ROVE.
 42
                                   , SUPERVISORY DATA. COMMANDS TO IA OR CRT.
 43 00215 000323 SPDT0.
                          212.
                                   , INPUT DONE (1).
 44 00216 000324 IDNE1:
                                   , SELECT DONE. SEE NOVA INSTRUCTIONS.
 45 00217 000326 SELD. .
                           214.
                                   , INTERRUPT TO CPU.
  46 00220 000327 INTR.
                          215.
  47 00221 000330 SELB.
 48 00222 000331 ODNE1: 217.
                                  ) OUTPUT DONE (1).
                                   , OUTPUT BUSY (1).
 49 00223 000332 OBSY1.
                           218.
                                  ... M NORMAL, GUTPUT M PULSE DUE TO ALGORITHM.
  50 00224 000333 NN..
                           219.
                                  JX MC. OUTPUT X FULSE DUE TO MICRO COMPUTER
  51 00225 000334 NMC. .
  52 00226 000335 SGN. :
  52 00227 000336 SGMC...
                           222.
  54 00230 000337 YN.:
                           223
     00231 000340 YMC.
                           224.
    00232 000341 SFDT1.
                          225.
                                   , SUPERVISORY DATA (1).
                                   ; TABLE DATA, ON INTERCONNECTION DIAG, THIS
     00233 000342 TDTA.
```

;SIGNAL IS TEL DTA/H.

59 00234 000344 TDTA. . 228.

ON INTERCONNECTION DIAGO THIS SIGNAL AWI4E1012 A

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0004 . MAIN
                                    JIS TEL DTAZL
                                    , ON INTERCONNECTION DIAG, THIS
   2 00235 000345 TBSY.
                            229.
                                    JSIGNAL IS TEL BSY/L
  \Theta \subseteq
                                    ;TABLE BUSY ON INTERCONNECTION DIAG,
                            231.
  04 00236 000347 TESY
                                    , THIS SIGNAL IS TEL BSY. H.
  95
                                    ON INTERCONNECTION DIAG. THIS SIGNAL
  06 00237 000350 NSTF
                            232
                                    ; IS WSTEP 1/P.
  97
                                    ,ON INTERCONNECTION DIAG, THIS SIGNAL
  08 00240 000352 XSTF.
                            204.
                                    JIS MSTEP 1/N.
                                    , ON INTERCONNECTION DIAG, THIS SIGNAL
  10.00241 000354 MSGN
                            236.
                                    ; IS NSIGN 1/L.
  11
                                    ON INTERCONNECTION DIAGOTHIS SIGNAL
                            238.
  12 00242 000356 MSGN
                                    , IS MSIGN 1.H.
  1.3
                                    ON INTERCONNECTION DIAGOTHIS SIGNAL
                            240.
  14 00243 000160 YSTP
                                    JIS MSTEP 1/P.
  15
                                    , ON INTERCONNECTION DIAG, THIS SIGNAL
  16 00244 000362 YSTP. .
                            242
                                    JIS YSTEP IVN.
  1.7
                                    ON INTERCONNECTION DIAG, THIS SIGNAL
  18 00245 000364 YSGN
                            244.
  19
                                    JIS YSIGN 1/H.
                                    ON INTERCONNECTION DIAG. THIS SIGNAL
  20 00246 000366 YSGN.
                            246.
                                    , IS Y5IGN 1/L
  21
                                    , INTERACTIVE CONTROLLER DATA (SUPERVISORY
                            248.
  22 00247 000070 ICDT.
                                    , CHANNEL). XX IGDTA 1/L.
                                    , ON INTERCONNECTION DIAG, THIS SIGNAL
                            250.
  -24 00250 000072 ICDT
                                    ; IS IODTA 1.H.
                                    ON INTERCONNECTION DIAG. THIS SIGNAL
                            252
  26 00251 000374 ICBY.
                                    , IS IOBSY/L
                                    FINTERACTIVE CONTROLLER BUSY. ON INTER-
  28 00252 000376 ICBY
                            254.
                                    , CONNECTION DIAG, THIS SIGNAL IS 1085Y/H.
  :3
  30 00253 000402 IABY.
                            258.
                                    . IA BUSY.
  31 00254 000406 IOMHZ
                            262.
                            265
                                    "DIAGNÓSTICS 10.
  32 00255 000411 DG10.
                                    , DIAGNOPTICS 9.
  DD 00256 000412 DG9.
  34 00257 000413 DG8.
                                    , DIAGNOSTICS 8.
                            267
                                    SHIFTING FREQ.
  35 00260 000414 IN:
                            268.
                                    , MODE NOT SET.
  36 00261 000454 MNS
                            300.
                                    GORT OR TEL MODE. 1=TEL
                            301.
  37 00262 000455 CT.
  38 00263 000456 ARC
                            302.
                                    ;ARC MODE SET = 1.
                            003.
                                    , MODE SET.
  39 00264 000000 MS:
                                    ; CNTR MODE SET = 1
  40 00265 000460 CNTR
                            ZØ4.
                                    JEND OF BUFFER.
  41 00266 000461 EOB.
                            305.
                            396.
                                    DELIMITER.
· 42 00267 000462 DLMR.
                            307
  43 00270 000463 CNTR.
                                    , CLK ACC/DEC.
                            308.
  44 00271 000464 CAD
                            ISS.
                                    ,CLK DIRECTION.

    45 00272 000465 CLDIR.

                                    , SINGLE WORD OUT.
  46 00273 000466 SWO.
                            310.
                                    , MICRO COMPUTER RST. LOGICAL "OR" OF
  47 00274 000467 MRST.:
                            311.
                                    , IORST, ORST AND DLMR.
  48
                                    /SINGLE WORD F/F (0).
                            212.
  49 00275 000470 SWD0
                            322
  50 00276 000502 DCHR.
                                    COMPLETION, INDICATES THAT A WORD OVER
  '51 00277 000505 COMP
                            325.
                                    , SUPERVISORY CHANNEL IS SENT OUT.
  52
                            326.
                                    , NON VECTORING LOGIC.
  53 00300 000506 NVL.
                                    FMC TAKE.
                            328.
   74 00001 000510 MTKE
                            029.
                                    CLK OUTPUT DONE
     00302 000035 CODNO
                            BB0.
                                    SINGLE WORD (1)
     00303 000512 SWD1
  57 99304 999513 OMSKO
                            331.
                                    , OUTPUT MSKO GUT.
                                                                           AWI4E1012 A
  58 00305 000514 XCLK.
                            332
                                    . XMIT CLK.
```

59 00306 000533 ORST.

347.

REV

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```
0005 . MAIN

    1 00207 000540 COMP.

  ,2 00310 000541 TBL..
                            354.
  03 00311 000542 TBL.
  04 00312 000543 PU1:
                            355.
                                    , PULL UP #1
  05 00313 000544 PU2:
                            356.
  06 00314 000545 PUB:
                            357.
  07 00315 000546 PU4
                            358.
                                    FINTERACTIVE CONTROLLER'S PROM IS ENABLED
  08 00316 000633 ICROM:
                            411.
  09 00317 000700 VCC.
                                    -; <del>-</del>534, 543, 645-697, 750-788, 1078, 1080, 1082,
                            44E.
                                    JINPUT MASKED OUT.
  10 00320 001035 IMSKO:
                           541.
                            588.
                                    JUSE OF VELOCITY ONTR IN ACCIDED.
 -11 00321 001114 CO:
  12 00022 001115 C1:
                            589.
                            590.
  13 00323 001116 C2:
 14 00324 001117 CZ.
                            591
  15 00325 001120 04:
                            592.
                            593.
  16 00326 001121 05:
  17 00327 000547 GND.
                            359.
                                    -; -410, 412-447, 544, 594-644, 789-825, 1077, 1079,
  18
                                    ) 1081, 1083.
                                    JOLK TMP.
  19 00330 001515 CTMP. :
                           845.
  20 00II1 001516 TMC. :
                           846.
                                    FIEMP MUN CONTROL
                          847.
- 21 00002 001517 TMP00:
  22 00III 001520 TMP01.
                            848.
                                    , MSB OF TEMPORARY STORAGE REG
                                    JIN MICRO COMPUTER.
  24 00334 001521 TMP02.
                           849.
  25 00335 001522 TMP03:
                           850.
  26 00336 001523 TMP04:
  27 00337 001524 TMP05:
                           852
                           853.
  28 00340 001525 TMP06:
 29 00341 001526 TMP07.
                           854.
                           855.
  30 00342 001527 TMP08:
  31 00343 001530 TMP09:
                           856.
  32 00344 001531 TMP10:
                           857.
  33 00345 001532 TMP11.
                           858.
  34 00346 001533 TMP12:
  35 00347 001534 TMP13:
                           860.
  36 00350 001535 TMP14:
                           861.
  37 00351 001536 TMP15.
                           862.
  38 00352 001537 RAM00:
                           863.
                                    SMSB OF RAM STORAGE IN MICRO COMPUTER.
  39 00353 001540 RAM01:
                           864.
  40 00354 001541 RAM02.
                           865.
  41 00355 001542 RAM03:
                            866.
 42 00356 001543 RAM04:
                           867.
  43 00357 001544 RAM05:
                           868.
  44 00360 001545 RAM06:
  45 00361 001546 RAM07:
                           870.
  46 00362 001550 RAM09.
                           872.
  47 00363 001551 RAM10:
                           873.
  48 00364 001552 RAM11.
  49 00365 001553 RAM12:
 50 00366 001554 RAM13.
                           876.
  51 00367 001555 RAM14:
                           877.
  52 00370 001556 RAM15.
                           878.
                                    TEMP MULTIPLEXER CONTROL.
  53 00371 001557 TMC.
                           879.
  ~4 00372 001560 A0:
                                    COUTPUT OF PROM-A. INDICATES ADDRESS OF OP
                                    ; CODE.
  3 00373 001561 A1:
                           881.
  57 00374 001562 A2:
                           882.
```

UR/W RAM. MC STORAGE

58 00375 001563 A3:

59 00376 001564 RWRM1. 884.

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0006 .MAIN
                                 CARRY OUTPUT FROM ALU.
   00377 001565 OCRY:
                                ; MODE CONTROL FOR ALU. SEE TI 74181
                         886.
∋2 00400 001566 M:
                                 GOP CODE BIT FOR ALU. SEE TI 74181.
03 00401 001567 50:
                         887.
04 00402 001570 51:
                         888.
                         889.
05 00403 001571 52:
06 00404 001572 S3:
                         890.
                                , CARRY IN. CARRY FROM PROM
07 00405 001573 CIN
                         891.
                                 .,ALU OUTPUT 00. (MSB).
                         892.
08 00406 001574 ALU00
09 00407 001575 ALUG1:
                         890.
10 00410 001576 ALU02.
                         894.
11 00411 001577 ALU03
12 00412 001600 ALU04:
13 00413 001601 ALU05.
                         897.
14 00414 001602 ALU06.
                         898.
15 00415 001603 ALU07:
16 00416 001604 ALUGS.
17 00417 001605 ALU09
                         901.
18 00420 001606 ALU10:
                         902.
19 00421 001607 ALU11:
                         905.
20 00422 001610 ALU12:
21 00423 001611 ALU13
22 00424 001612 ALU14.
                         906.
                                 #ALU OUTFUT 15. (LSB)
23 00425 001613 ALU15
                         907.
                                 DEQUALITY, ALU'S OUTPUT.
                         908.
24 00426 001614 E0 E3:
                                 RET SKIP CONDITION.
25 00427 001615 SRST. .
                         909.
                                - FEAD LATCH
                         910.
26 00430 001616 RDL.
27 00431 001617 TRAP.:
                         911.
                                 J/Z AND MRST.
78 99432 001620 NMRT.:
                         912
913.
                                 DATA CHANNEL OUT DUE TO READ.
30 00434 001622 RDCO.:
                         914.
                                 JEXAMINE PULSE FROM FRONT PANEL
31 00435 001623 EMFL
                         915.
                                 JADV. ROM ADDR. COUNTER.
                         916.
32 00436 001624 ARAC.
                                 , PROCEED.
                         917.
33 00437 001625 PRCD.
                                 JSTOP MC.
34 00440 001626 SMC.
                                 READ INST.
35 00441 001627 RD. .
                         919.
                                 , PULSE INST.
36 00442 001630 PL..
                         920.
37 00443 001631 RSRT.
                         921.
                                 RESTART INST.
                                 DEC LDC 17 IN RAM
38 00444 001632 DE17.:
                         922.
                         923.
                                 ; RUN-STOP.
39 00445 001633 RNSTP:
                                 , PULSE FROM FRONT AFTER EXAMINE PULSE
                         924.
40 00446 001634 DXPL..
                                 FRUN PULSE, OCCURS WHEN SWITCHING FROM
41 00447 001635 RNPL.
                         925.
                                 STOP TO RUN.
42
                                 JEXAMINE PULSE
 43 00450 001636 EXPL :
                                 ; INTERNAL PULSE
44 00451 001637 INPL :
                                 , RESET MC OR READ INST.
45 00452 001640 RMRT. . 928.
                                 ; INHIBIT STORAGE IN TEMP.
 46 00453 001642 I.
                         930.
                                 STORE IN RAM.
 47 00454 001643 ST:
                         931
                                 CLR MZ AND MY STORAGE.
 48 00455 001644 XCLR...
                         932.
                                 PULSE WHICH PUTS THE PROGRAM BACK IN LOOP
 49 00456 001645 XX..
                                 ; AFTER PULSE TO IA VIA ALGORITHM.
.50
                         934.
 51 00457 001646 ENDO. :
                                  PULSE WHICH PUTS THE PROGRAM BACK IN LOOP
 52 00460 001647 XY. :
                                  PAFTER PULSE TO IA VIA PULSE INST.
 53
                                  ; CLOSURE PULSE.
                         936.
 : 00461 001650 CSRP.
                                  FEFFECTIVE JUMP, JUMP INST OR CONDT. JUMP.
                         937.
   00462 001651 EJMP.:
                         938.
  3 00463 001652 MC5.
                                  , ROM ADDR. COUNTER LOADED.
                         939.
 57 00464 001653 RACL.:
                                                                     AWI4E1012
 58 00465 001654 RWRM0:
                         940.
```

59 00466 001655 JUMP:

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0007 MAIN
                                 , DEC LOCATION 17 IN MC RAM
 L 00467 001656 DE17
ප2 00470 001657 SKIP. :
                         943.
                                 ISTART, BEGINNING OF NEW INST. MRST OR PO
03 00471 001660 STRT.:
                         944.
                                 DECREMENT, MC INSTR.
                         945.
04 00472 001661 DE...
                                 DIRECTION OF CIRCLE
05 00473 001662 W:
                         946.
06 00474 001663 P2
07 00475 001664 P7:
                         948.
08 00476 001665 F9
                         949.
                         959.
                                 ⇒X START.
09 00477 001666 WS
                         951.
                                 ⇒Y START.
10 00500 001667 YS:
                         952.
11 00501 001670 FS:
                                 START DECEL
                         953.
12 00502 001671 SDEC
                                 J X END.
13 00503 001672 ME.
                         954.
                                 JY END.
14 00504 001673 YE:
                         956.
15 00505 001674 CMS:
                         957.
16 00506 001675 CF.
                         958.
                                 ⇒∷ START.
17 00507 001676 XS.
18 00510 001677 YS. :
                                 BY START.
                         959.
19 00511 001700 FS. .
                         960.
20 00512 001712 W. :
                         979.
                         971.
21 00513 001713 LXY.
22 00514 001714 CXY.
23 00515 001715 CA:
24 00516 001716 LA:
                         974.
                         975.
25 00517 001717 GXY.:
26 00520 001720 ARC.
                         976.
                         978.
                                  3 10MHZ CLK.
27 00521 001722 IOMHZ:
28 00522 001753 PO.:
                         100I.
29 00523 001754 P1 :
                         1004.
30 00524 001755 P2.:
                         1005.
31 00525 001756 P3. .
                         1006.
32 00526 001757 P4.:
                         1007.
33 00527 001760 PS.:
                         1998.
34 00530 001761 P6. .
                         1009.
35 00531 001762 P7. .
                         1010.
                         1011.
36 00532 001763 P8.:
                         1012.
                                 GPHASE 9.
37 00533 001764 P9. .
                                 ; PULSE LATCH (1).
38 00534 001765 FLL1.
                         1013.
                                 DEC LOCATION 17 OR PULSE INSTRUCTION.
29 00525 001766 DE17P:
                        1014.
                                 DEC LOCATION 17 LATCH.
                        1015.
40 00536 001767 DE17L:
                         1024.
                                 GRUN INHIBIT ACC/DEC
41 00537 002000 RIAD:
                         1025.
                                 /DIAG. 14.
42 00540 002001 DG14.:
                                 ; SIGNAL FROM MC WHICH PRECEDES RAMP DWN
                         1026.
43 00541 002002 CF.:
                                  COMMAND.
44
                                 ; END INST OR MRST.
                         1027.
45 00542 00200B ERST.
                         1028.
46 00543 002004 DG13.:
                                 START DECEL. COMMAND FROM MC TO ACC/DEC
                         1029.
47 00544 002005 SDEC.
                                  ;LOGIC TO START RAMP DWN.
48
                                 ,START ACC/DEC LOGIC (DEC LOC 17 AND TBL).
49 00545 002006 SAD:
                         1030.
50 00546 002007 DG15 :
                         1031.
                         1032.
                                 DUN. ACC/DEC IS RAMPING DUN
51 00547 002010 DN:
                         1033.
52 00550 002011 UP
                                 , TIMER OVERFLOW ACC/DEC LOGIC.
53 00551 002012 TO.
                         1024.
                         1035.
                                 STOP VELOCITY COUNTER.
 4 00552 002013 STPV.
                                  JEND INST OR MRST.
                         1036.
 5 00553 002014 ERST:
                                 FOR SPEED LIMIT. OUTPUT OF ACCYDED LOGIC.
56 00554 002015 TSL.
                         1037.
                                 #ACC/DEC OUTFUT.
                         1038.
57 00555 002016 TICK:
                                 SELECT A.
58 00556 002020 SLCTA.
                         1040.
                                                                       AWI4E1012 A
```

SELECT B.

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59 00557 002021 SLCTB: 1041.

```
0008 . MAIN
                                 CLK ROM STORAGE. USED TO STORE A & C PROMS
  t 00560 002023 CLKRS. 1043.
                                  , OUTFUT.
  52
 03 00561 002024 GA:
                          1044.
 04 00562 002025 GXY:
                          1045.
                                  ;ROM ADDRESS COUNTER BIT Ø (MSB).
 05 00563 002026 RAC0
                          1046.
 06 00564 002027 RAC1
                          1047.
                          1048.
 07 00565 002030 RAC2
 08 00566 002031 RAC3
                          1049.
 09 00567 002032 RAC4
                          1050.
 10.00570 002033 RAC5
                          1051.
 11 00571 002034 RAC6:
                          1052.
                          1053.
 12 00572 002035 RAC7
                                  LOAD ROM ADDR. COUNTER
                          1054.
 13:00572 002036 LRAC.
                                  JOLK TEMP STORAGE.
 14 00574 002040 CTMP:
                          1056.
                          1057.
                                  ; PULSE GUT.
 15 00575 002041 FLOUT:
                                  CLOSURE LATCH.
 16 00576 002042 CSRL1:
                          1058.
 17 00577 002043 CSRL0:
                          1059.
                                  ; NEGATIVE TEST OR JUMP.
 18 00600 002050 NTJ. .
                          1064.
 19 00601 002051 EQ.
                           1065.
 20 00602 002052 CARRY.
                          1066.
                                  , FULL UP S VALID ON W. L. REV D & UP.
                           1067.
- 21 00603 002053 FUS
                                  , PULL UP 10 VALID ON W.L. REV D & UP.
 22 00604 002054 PU10.
                          1068.
                                  , PULL UP 11 VALID ON W. L. REV D & UP.
 23 00605 002055 PU11:
                          1069.
                                  ) PULL UP 12 VALID ON W.L. REV D & UP.
- 24 00606 002056 PU12.
                          1079.
                          1071.
1 25 00607 002057 DBL.:
                                  HALT INST. IN MICRO COMPUTER, USED FOR
  26 00610 002060 HALT. .
                          1072.
                                   DIAG. ONLY.
  27
                           1073.
 28 00611 002061 PU5.
                          1074.
 29 00612 002062 PU6:
                           1075.
  30 00613 002063 PU7.
  31 00614 002064 PUS.
                          1076.
                           1084.
  32 00615 002074 PO.
                                  READ LATCH AND PØ. SIGNAL WHICH GEN DATA
  33 00616 002075 RLF0:
                          1085.
                                  CHANNEL SYNC WHEN MC NEEDS DATA.
                                  MRIST AND PHASE 1.
                          1086.
 35 00617 002076 MRP1.:
                          . END
  36
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